

FIG.1

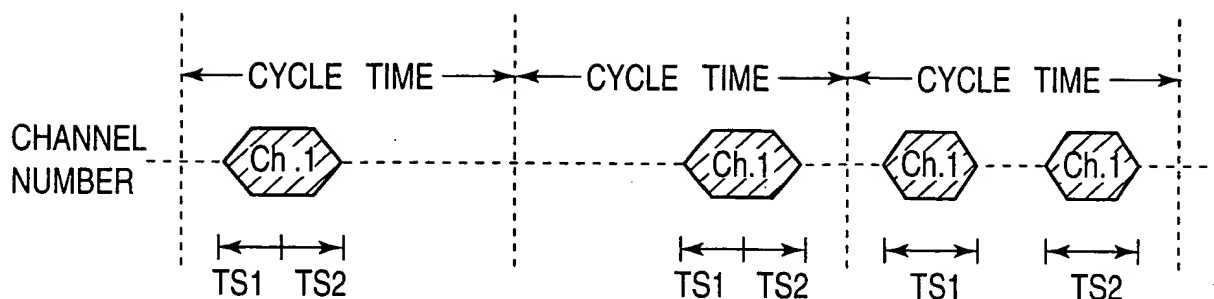


FIG. 2

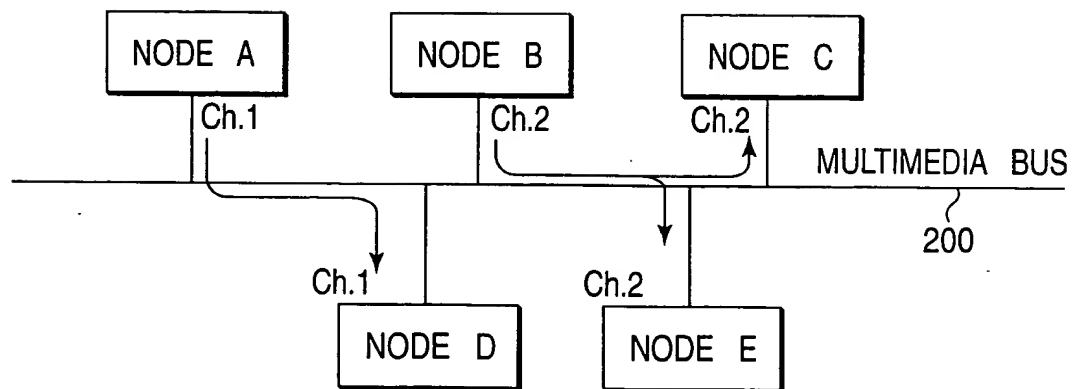


FIG. 3

CHANNEL CONTROL REGISTER IN CONFIGURATION

Config Add.	Ch Cnt.(8bit)	Ch No.(8bit)	Necessity (16bit)
10		2	123
14		3	456
18		4	789

7	6	5	4	3	2	1	0
Ch Ava	In/Out	Reject					

FIG. 4

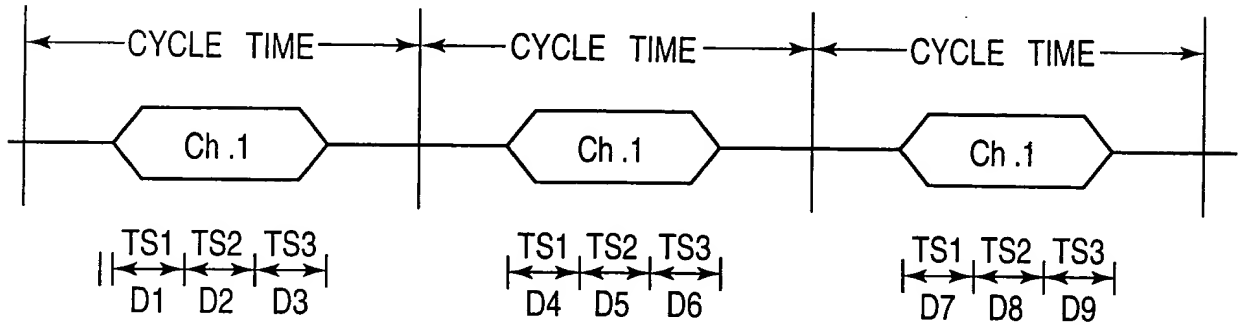


FIG. 5A

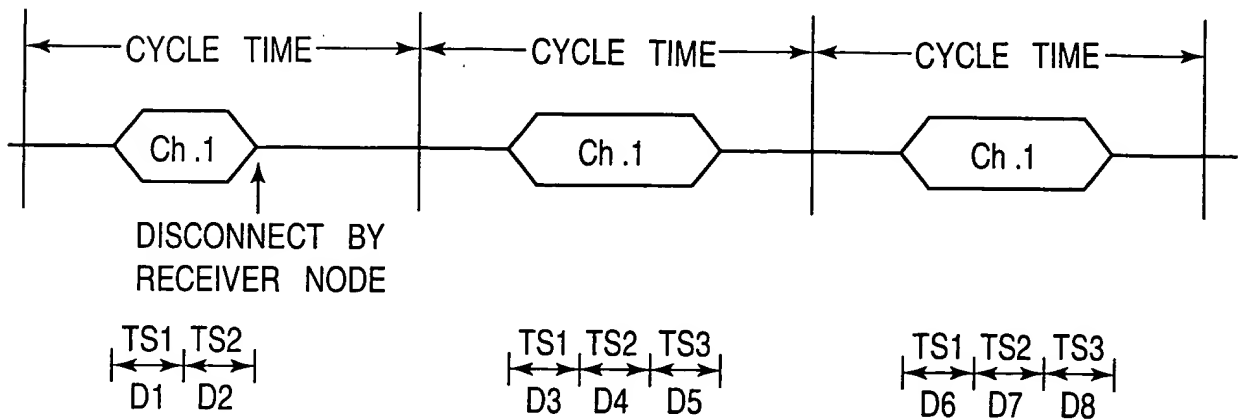


FIG. 5B

FLOW CONTROL OF STREAM ACCESS

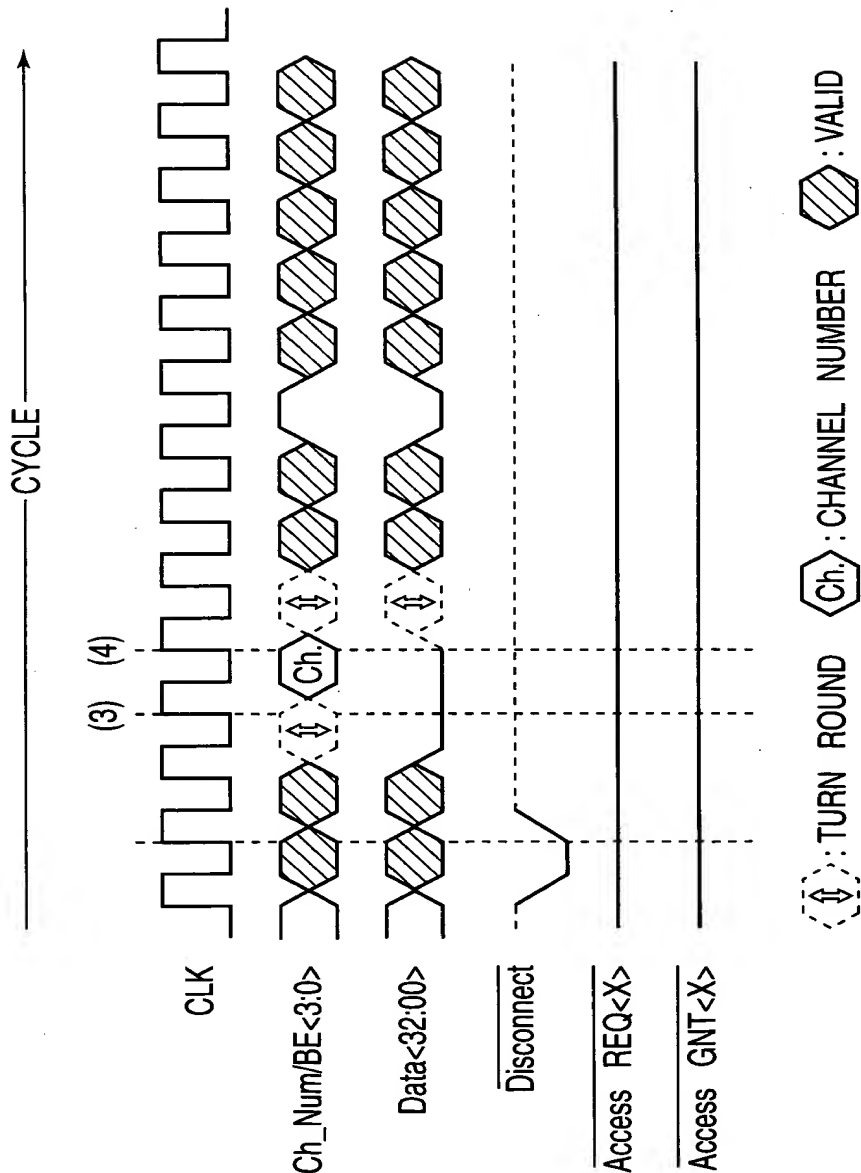
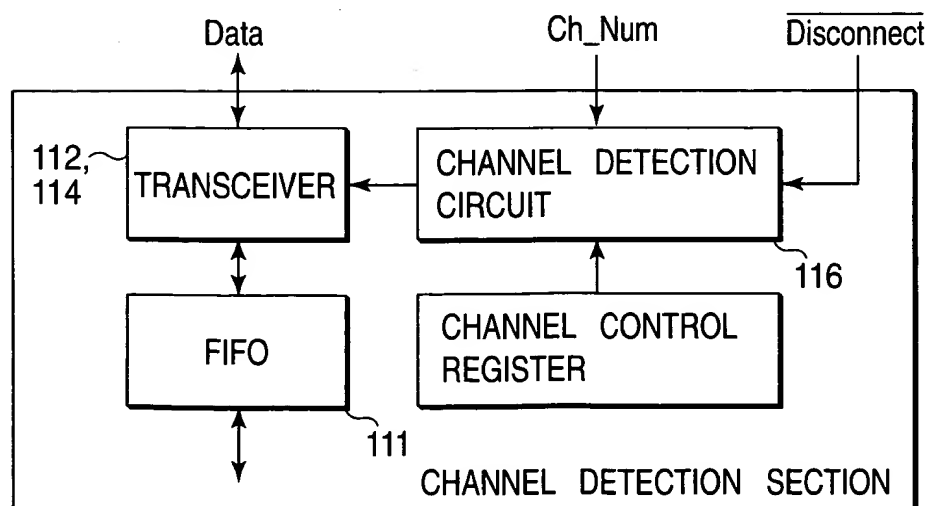
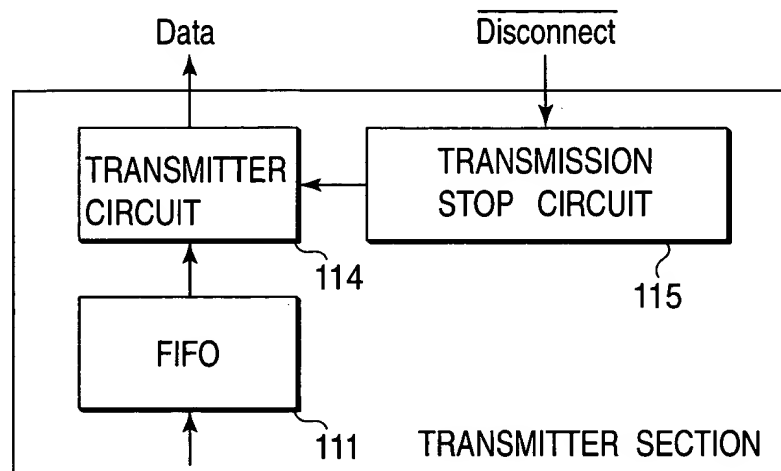
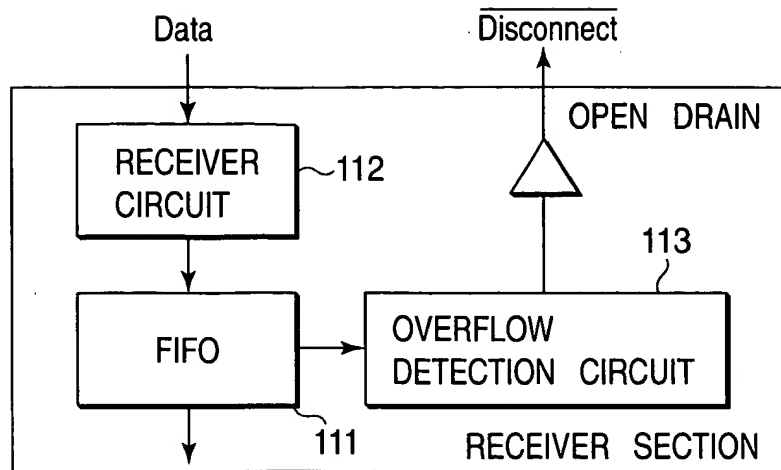


FIG. 6



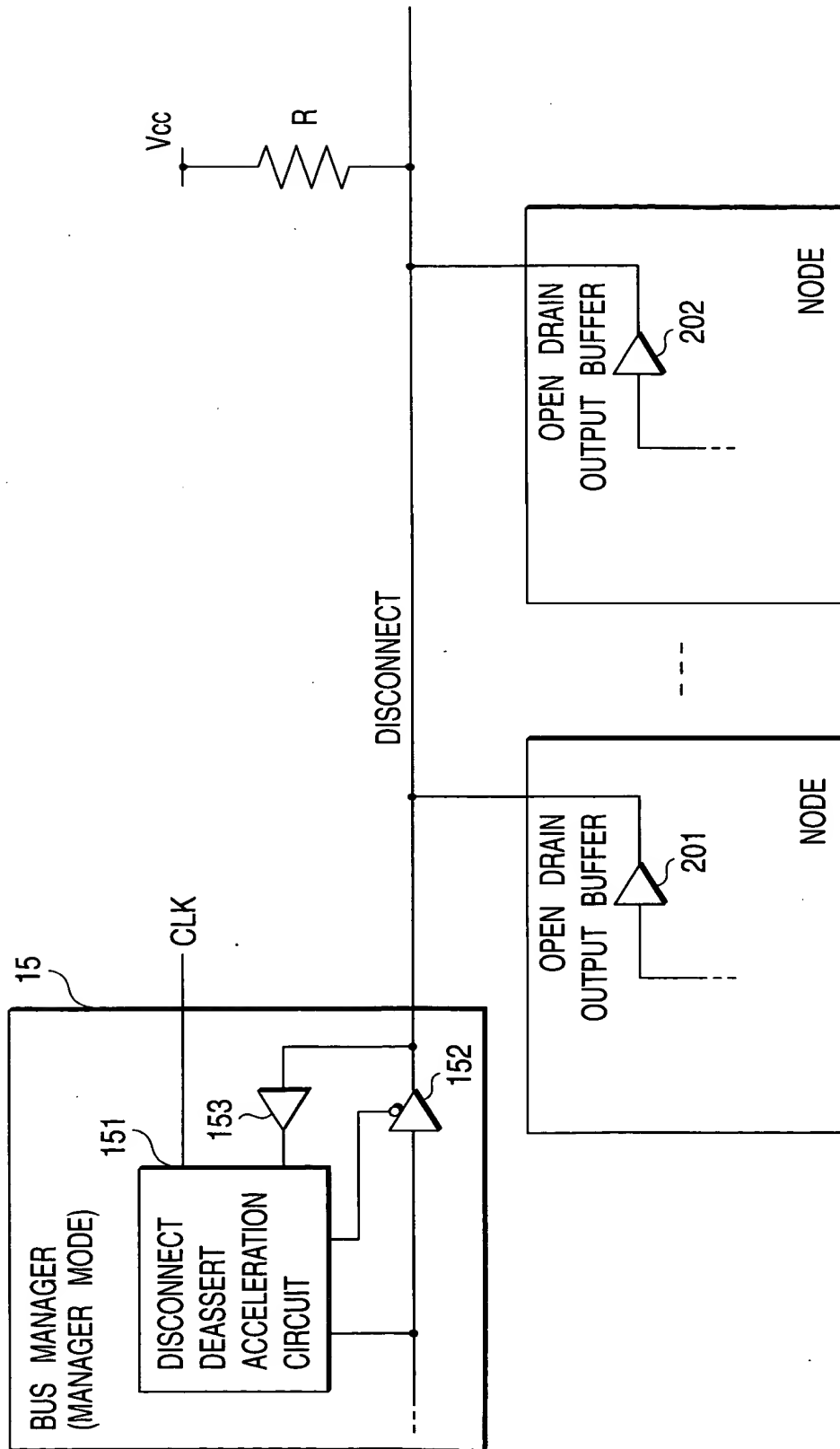


FIG.9

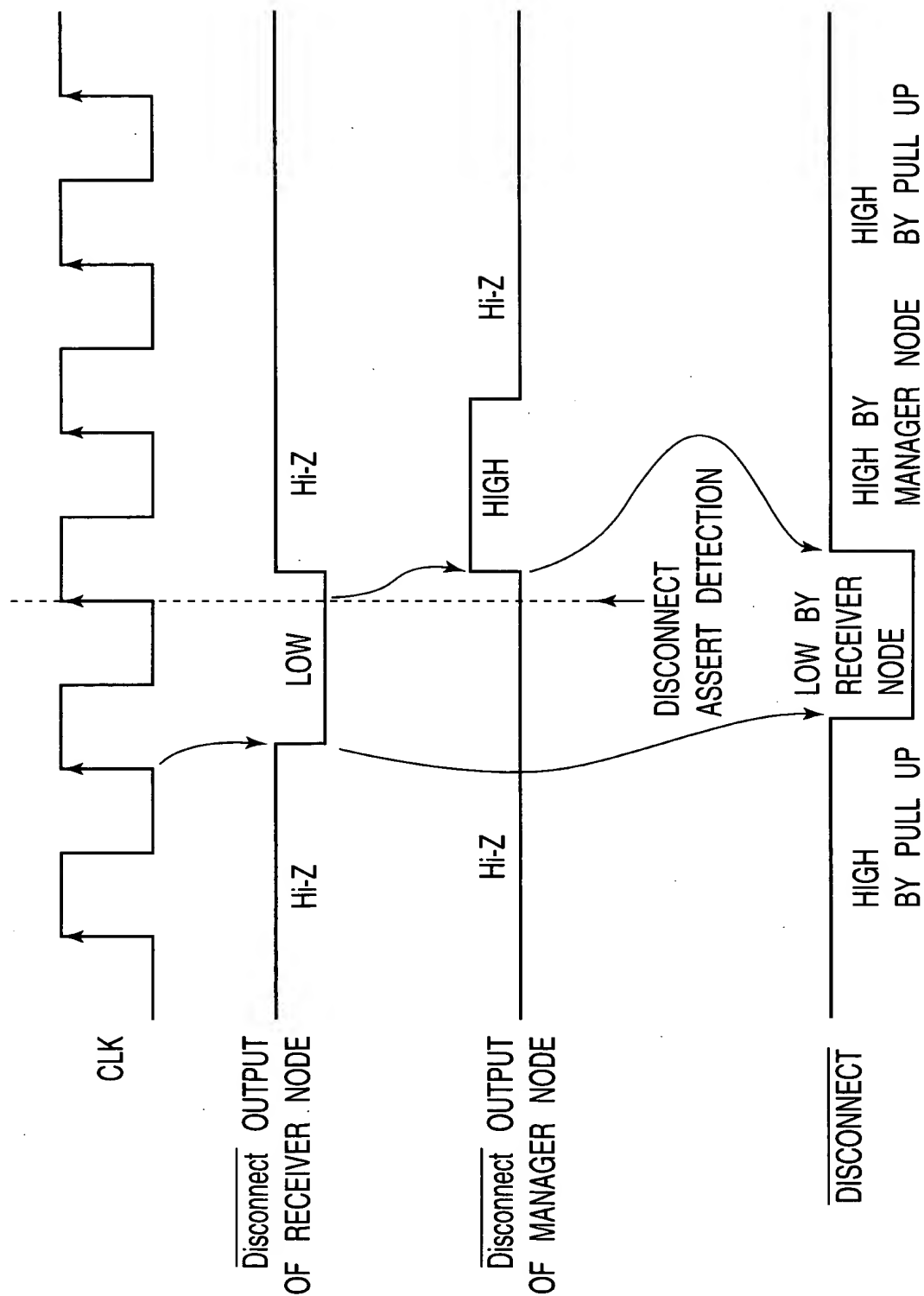


FIG. 10

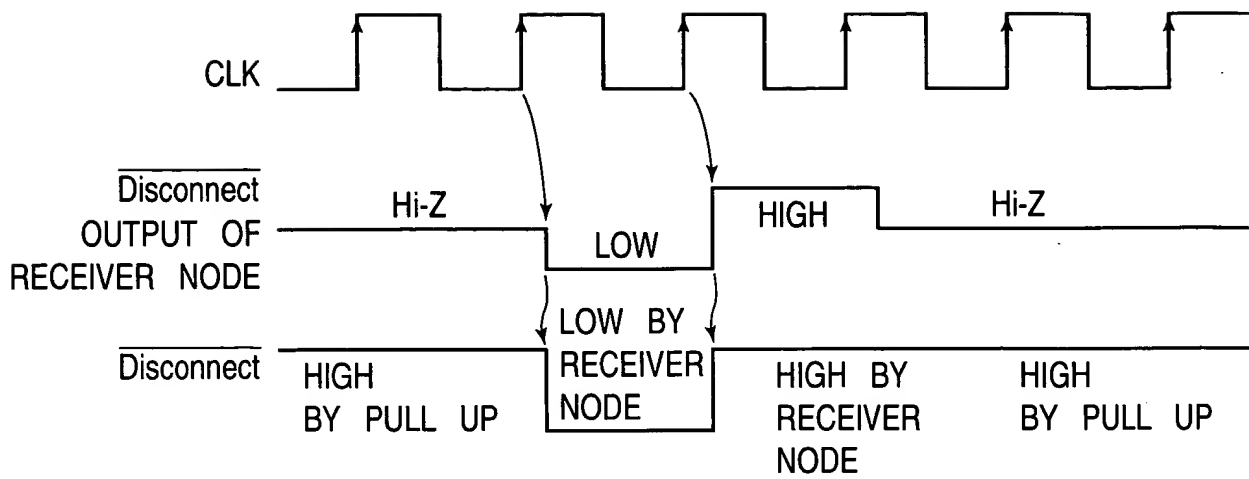


FIG. 11

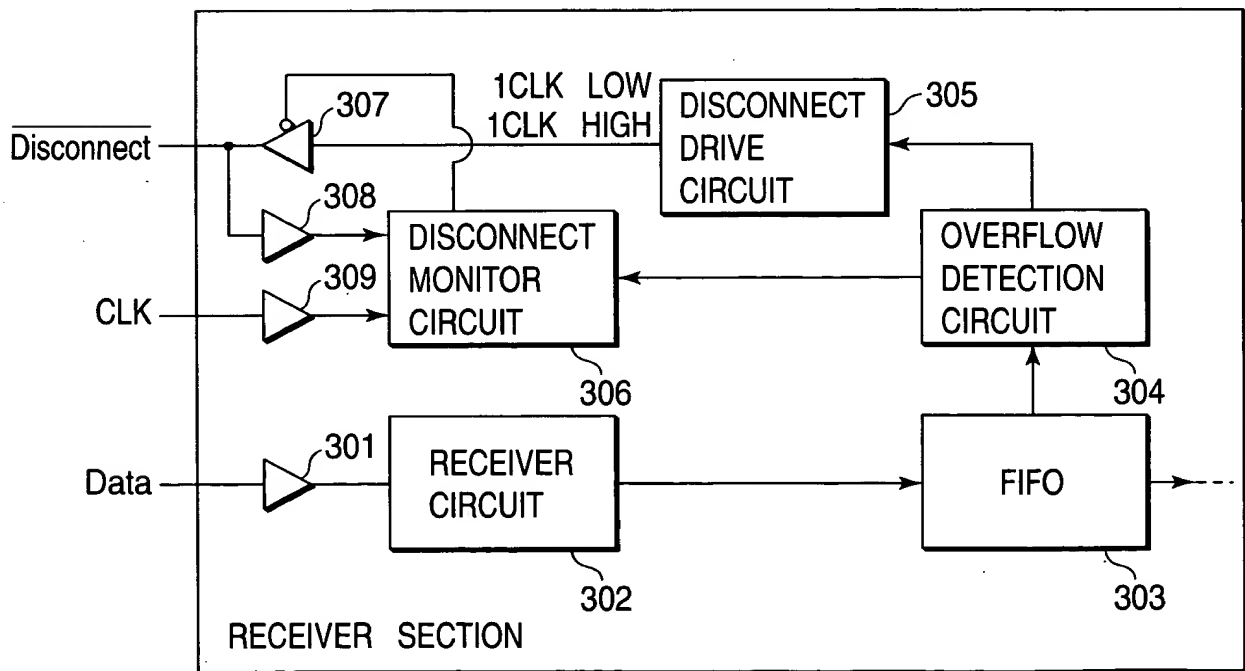


FIG. 12